

1 FW 2115

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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	09/978,357	
	Filing Date	10/16/2001	
	First Named Inventor	Carpenter	
	Art Unit	2115	
	Examiner Name	Mark CONNOLLY	
Total Number of Pages in This Submission	14	Attorney Docket Number	AUS920010957US1 (IBM 2364000)

ENCLOSURES (Check all that apply)		
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<input type="checkbox"/> Reply to Missing Parts/Incomplete Application	<input type="checkbox"/> Landscape Table on CD	
<input type="checkbox"/> Reply to Missing Parts under 37 CFR 1.52 or 1.53	<b>Remarks</b> Reply to Office Action dated November 4, 2004 (13 pages)	
<b>SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT</b>		
Firm Name	CARR LLP	
Signature		
Printed name	Theodore F. Shiells	
Date	2/4/05	Reg. No. 31.569

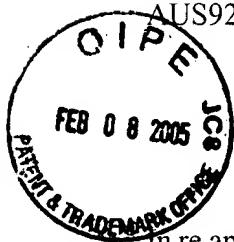
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AUS920010957US1 (IBM 2364000)

PATENT APPLICATION  
SERIAL NO. 09/978,357



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Carpenter et al.

Serial Number: 09/978,357

Filed: October 16, 2001

For: METHOD OF SYNCHRONIZATION  
BETWEEN HIGH SPEED PROCESSOR  
DEVICE CONFIGURATION INTERFACE  
AND LOW SPEED DEVICES

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Group Art Unit: 2115

Examiner: Mark Connolly

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2/4/2005

*Bradley D. Ellis*

Bradley D. Ellis

RESPONSE

Dear Sir:

In response to the Office Action dated November 4, 2004 ("Office Action"), Applicants respond as follows: